

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	2366633
<b>Application Number:</b>	10776702
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	3432
<b>Title of Invention:</b>	SRAM CONTROLLER FOR PARALLEL PROCESSOR ARCHITECTURE INCLUDING AN ADDRESS AND COMMAND QUEUE AND METHOD FOR CONTROLLING ACCESS TO A RAM
<b>First Named Inventor/Applicant Name:</b>	Matthew J. Adiletta
<b>Customer Number:</b>	20985
<b>Filer:</b>	Denis G. Maloney/Jennifer Payne
<b>Filer Authorized By:</b>	Denis G. Maloney
<b>Attorney Docket Number:</b>	10559-078003
<b>Receipt Date:</b>	24-OCT-2007
<b>Filing Date:</b>	10-FEB-2004
<b>Time Stamp:</b>	22:44:50
<b>Application Type:</b>	Utility under 35 USC 111(a)

### Payment information:

Submitted with Payment	yes
Payment was successfully received in RAM	\$ 180
RAM confirmation Number	3976
Deposit Account	061050

### File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
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<b>Total Files Size (in bytes):</b>			51233788		
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